

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	38	(multilayer\$3 or "multi-layer" or "multi layer") with card with (smart or ic or processor or microprocessor or "integrated circuit" or chip) same (antenna or coil)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/13 11:09
L2	97	card with "reinforcing plate"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/13 11:30
L3	52	(layer or multilayer\$3 or "multi-layer") with card same (antenna or coil) same (ic or "integrated circuit" or module or chip or microprocessor or processor) same (reinforc\$4 or plate)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/13 11:50
L4	3	(layer or multilayer\$3 or "multi-layer") with card same (antenna or coil) same (ic or "integrated circuit" or module or chip or microprocessor or processor) same (reinforc\$4 or plate) same hole	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/13 11:50
L5	44	(layer or multilayer\$3 or "multi-layer") with card same (antenna or coil) same (ic or "integrated circuit" or module or chip or microprocessor or processor) same hole	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/13 11:50
L6	44	4 5	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/13 11:50
L7	7	("4822988" "5612532" "5671525" "5903239" "6412702" "6556175" "6568600").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/12/13 11:54
L10	35	layer with card with hole same (ic or chip or "integrated circuit" or processor or circuit or microprocessor or smart) same (antenna or coil or reinforc\$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/13 12:04
L11	72	(film or sheet or layer) with card with hole same (ic or chip or "integrated circuit" or processor or circuit or microprocessor or smart) same (antenna or coil or reinforc\$3)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/13 12:21

L12	5793	235/492.ccls. 235/487.ccls. 235/493.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/13 12:30
L13	13	12 and (layer or multilayer\$3 or "multi-layer" or "multi-layered" or "multi layered" or "multi layer") with hole same (ic or chip or processor or microprocessor or circuit or "integrated circuit" or smart) same card same (antenna or coil)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/13 12:31
L14	73	card with sheet with thermoplastic same (ic or chip or processor or "integrated circuit" or microprocessor)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/13 13:06
L15	9	("5821614" "6008993" "6012641" "6076737" "6095423" "6186398" "6422468" "6580481" "6607135").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/12/13 13:14
L16	1	"4483067".pn.	US-PGPUB; USPAT; USOCR	OR	ON	2005/12/13 13:14
L17	125	(polycarbonate or thermoplastic or pvc) with card with layer same (ic or smart or "integrated circuit" or chip or smart or microprocessor or processor)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/13 14:15
L18	0	2001-319210.pn.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/13 16:33
L19	1	"2001319210".pn.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/13 16:33
S1	1285	(card or carrier) same (substrate or sheet or core or layer) with (ic or "integrated circuit" or chip or processor or microprocessor) with (sandwich\$4 or surround\$4 or envelop\$4 or cavity)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/12 15:34
S2	2776	(card or carrier) same (substrate or sheet or core or layer) with (ic or "integrated circuit" or chip or processor or microprocessor) with (sandwich\$4 or surround\$4 or envelop\$4 or cavity or hole or recess)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/12 15:34

S3	2234	(card or carrier) with (substrate or sheet or core or layer) with (ic or "integrated circuit" or chip or processor or microprocessor) with (sandwich\$4 or surround\$4 or envelop\$4 or cavity or hole or recess)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/12 15:34
S4	60	(card or carrier) with (substrate or sheet or core or layer) with (ic or "integrated circuit" or chip or processor or microprocessor) with (sandwich\$4 or surround\$4 or envelop\$4) with (cavity or hole or recess)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/12 15:36
S5	92	(card or carrier) with (substrate or sheet or core or layer or mount\$3) with (ic or "integrated circuit" or chip or processor or microprocessor) with (sandwich\$4 or surround\$4 or envelop\$4) with (cavity or hole or recess)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/12 16:02
S6	38	(card or carrier) with (substrate or sheet or core or layer or film) with (ic or "integrated circuit" or chip or processor or microprocessor) with (sandwich\$4 or surround\$4 or envelop\$4) with (hole)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/12 16:04
S7	64	(card or carrier) with (substrate or sheet or core or layer or film) with (ic or "integrated circuit" or chip or processor or microprocessor) with (sandwich\$4 or surround\$4 or envelop\$4) with (hole or cavity)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/12 16:13
S8	88	(card or carrier) with (substrate or sheet or core or layer or film) with (ic or "integrated circuit" or chip or processor or microprocessor) with (sandwich\$4 or surround\$4 or envelop\$4) with (hole or cavity or recess or opening)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/12 16:13
S9	131	(card or carrier) with (substrate or sheet or core or layer or film) with (ic or "integrated circuit" or chip or processor or microprocessor) with (sandwich\$4 or surround\$4 or envelop\$4 or stack\$3) with (hole or cavity or recess or opening)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/12 16:17
S10	131	S8 S9	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/12 16:41

S11	4960	235/492.ccls. 235/487.ccls.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/12 16:41
S12	336	S11 and "through hole"	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/12 16:52
S13	218	S11 and "through hole" and (chip or ic or microprocessor or processor) with (card or carrier or smartcard)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/12 16:42
S14	1	"thermoplastic resin" near3 multilayer same card	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/12 17:30
S15	7	"resin" near3 multilayer same card	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/12 17:30
S16	20	(US-20040108934-\$ or US-20040159709-\$ or US-20030226901-\$).did. or (US-6608391-\$ or US-5851854-\$ or US-5072283-\$ or US-4897534-\$ or US-4396971-\$ or US-6283378-\$ or US-5346576-\$ or US-5250341-\$ or US-6910636-\$ or US-6607135-\$ or US-6166911-\$ or US-4792843-\$).did. or (JP-2004171100-\$ or JP-2003308507-\$ or JP-08267973-\$ or JP-03166997-\$ or JP-11078324-\$).did.	US-PGPUB; USPAT; JPO	OR	ON	2005/12/12 17:35
S17	9	S16 and (antenna or coil)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/12 17:42
S18	4	card same (layer or sheet or film) same (reinforc\$4) near3 plate same core same (antenna or coil)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/12 17:45
S19	28	card same (layer or sheet or film) with (hole or opening or recess) same core same (antenna or coil)	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/12/12 17:45